

**TRIVANDRUM ENGINEERING SCIENCE AND TECHNOLOGY (TrEST) RESEARCH PARK**  
(A State Government Undertaking)  
**College of Engineering Trivandrum 695016**  
[trestpark@kerala.gov.in](mailto:trestpark@kerala.gov.in)

**APPLICATION FORM FOR RISC-V PROCESSOR CHIP DESIGN TRAINING**

**(Office use only)**  
Application No:

Date:

Paste a self -  
attested  
Photograph

**PERSONEL DETAILS**

1. Name of the Applicant	:	
2. Age & Date of birth	:	
3. Gender	:	
4. College/Institution	:	
5. Branch	:	
6. Class	:	B.Tech/M.Tech./Ph.D.
7. Specialization (for M.Tech./Ph.D.)	:	
8. Semester	:	
9. CGPA till date	:	
10. Address	:	
Email:		
Mobile:		

I..... of ..... college/institution hereby declare that all the particulars given above are true and correct to the best of my knowledge and belief.

Place:

Date:

Signature:

Recommended by:

Staff Advisor/HOD